What is claimed is:

1. A microcomputer including a plurality of peripheral circuits, comprising a connecting circuit that permits interconnection among the plurality of peripheral circuits to be controlled through execution of a program.

2. A microcomputer as claimed in claim 1, wherein the connecting circuit comprises a selector that selects one among a plurality of inputs and then outputs the selected input according to data given by the program.

3. A microcomputer as claimed in claim 1, wherein a circuit for writing data input to the peripheral circuits through execution of the program is provided as one of the peripheral circuits.

4. A microcomputer as claimed in claim 1, wherein a circuit for reading data output from the peripheral circuits through execution of the program is provided as one of the peripheral circuits.

5. A microcomputer as claimed in claim 1, wherein a circuit for inputting a signal from outside to the microcomputer is provided as one of the peripheral circuits.

6. A microcomputer as claimed in claim 1, wherein a circuit for outputting a signal generated inside the microcomputer to outside is provided as one of the peripheral circuits.

7. A microcomputer as claimed in claim 1, further comprising means for realizing a predetermined peripheral circuit function by controlling through the connecting circuit the interconnection among the plurality of peripheral circuits.